



UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Krishna Seshan

Title: SELECTABLE DECOUPLING CAPACITORS FOR INTEGRATED CIRCUIT AND METHODS OF USE

Docket No.: 884.380US2

Filed: December 2, 2003

Examiner: David Vu

Customer No.: 21186

Serial No.: 10/726,050

Due Date: July 19, 2005

Group Art Unit: 2818

Confirmation No.: 2687

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Notice of Allowance Date:

April 19, 2005

We are transmitting herewith the attached:

☒ A check in the amount \$ 1400.00 to cover the Large Entity Issue Fee Payment.

☒ A check in the amount \$ 3.00 to cover the Extra Patent Copies Fee (1 copies).

☒ Issue Fee Transmittal (Form PTOL-85).

☒ A check in the amount \$ 300.00 to cover the Publication Fee Payment.

☒ A return postcard.

☒ Other: Communication Concerning Related Application(s) (1 pg.).

Please charge any additional required fees for the Issue Fee Payment or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

By Ann M. McCrackin

Ann M. McCrackin

Reg. No. 42,858

AMM1:CMG:jdgw

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Attn - MAIL STOP ISSUE FEE, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of July, 2005.

John D. Goltz - Wrathell
Name

John D. Goltz - Wrathell
Signature



S/N 10/726,050

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Krishna Seshan
Serial No.: 10/726,050
Filed: December 2, 2003
Title: SELECTABLE DECOUPLING CAPACITORS FOR INTEGRATED CIRCUIT
AND METHODS OF USE
Assignee: Intel Corporation
Examiner: David Vu
Group Art Unit: 2818
Docket: 884.380US2
Customer No. 21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/792596 6686659	February 23, 2001	884.380US1	SELECTABLE DECOUPLING CAPACITORS FOR INTEGRATED CIRCUIT (As Amended)

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicant requests that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,
KRISHNA SESHAN
By his Representatives,
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Attorneys for Intel Corporation
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9592
Date July 19, 2005 By Ann M. McCrackin
Ann M. McCrackin
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of July, 2005.

John D. Gelfer-Wrathall
Name

John D. Gelfer-Wrathall
Signature